

Notice of Allowability	Application No.	Applicant(s)	
	10/667,321	OMORI ET AL.	
	Examiner	Art Unit	
	Hai C. Pham	2861	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. X This communication is responsive to 12/16/04.			
2. X The allowed claim(s) is/are 1-46.			
3. X The drawings filed on 23 September 2003 are accepted by the Examiner.			
 4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)			
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal P. 6. ☐ Interview Summary Paper No./Mail Dat 7. ☐ Examiner's Amendn 8. ☑ Examiner's Stateme 9. ☐ Other	(PTO-413), e nent/Comment	

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REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance. Claims 1, 15, 29-31, 38, 45 and 56 are patentable over the prior art patents and printed publications because of the specific pixel clock generation apparatus that includes a phase shift data generation part for outputting a phase shift data for controlling the phase shift amount of the pixel clock, the pattern of the phase shift data being stored in a lookup table based on a difference between a measured time interval between two horizontal synchronization signals as detected by a detecting circuit and a target value, wherein a pixel clock generating part generates the pixel clock whose phase is controlled by the phase shift data output from the phase shift data generation part based on a high frequency reference clock. The combined limitations are not taught by the prior art of record alone or in combination.

Claims 2-14, 16-28, 32-37 and 39-44 are allowed because they are directly or indirectly dependent from claims 1, 15, 31 and 38 above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

2. The information disclosure statements (IDS) submitted on 12/16/04, 12/29/04 and 01/26/05 are being considered by the examiner, and have been placed of record in the file.

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3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hai C. Pham whose telephone number is (571) 272-2260. The examiner can normally be reached on M-F 8:30AM - 5:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David L. Talbott can be reached on (571) 272-1934. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

HAI PHAM PRIMARY EXAMINER

Haidi Phan

March 18, 2005